



## Department of Placements and Training

### CIRCULAR

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We are happy to share that the 2018 batch students are eligible to participate in **“ANSYS”** drive for **“R&D Engineering Intern”** position.

#### Company Profile:

#### SUMMARY

This summer intern will join the RedHawk-SC Research and Development team. He or she will develop features and algorithms for the distributed computing infrastructure, graphical user interface and other advanced design-data-import and analysis capabilities in distributed framework.

Innovation in semiconductor design and manufacturing enables smaller device architectures with higher performance and energy efficiency for powering the smart product revolution. The physics associated with shrinking geometries, especially in the emerging 3-D IC, FinFET and stacked-die architectures, bring out design challenges related to power and reliability, affecting design closure. ANSYS simulation and modeling tools offer the sign-off accuracy and performance needed to ensure power noise integrity and reliability of even the most complex ICs, taking into account electromigration, thermal effects and electrostatic discharge phenomena.

#### RESPONSIBILITIES

- Learn the team’s software development processes.
- Diagnose and fix code problems.
- Deliver code and scripts that meet requirements on schedule. Ensure that code is efficient, scalable, maintainable, extensible, robust and easy to understand.
- Create unit, regression and/or system-level tests to thoroughly validate new features or changes.
- Communicate clearly and work closely with manager, technical leads and other engineers to refine solutions and to describe changes that may affect others.
- Learn and follow best practices in software engineering.

#### MINIMUM QUALIFICATIONS

- Progress toward BE / BTech or ME / MTech degree in Computer Science, Electrical Engineering, or related field
- Proficiency in Python, C or C++
- Working knowledge of the Linux operating system
- Strong basic knowledge of data structures, algorithms, and debugging
- Basic understanding of electronic design at gate level and/or transistor level

- Ability to learn quickly, understand complex systems and to work closely with others
- Ability to complete high-quality work on time

#### **PREFERRED QUALIFICATIONS**

Knowledge in the following areas are a plus:

- Commercial software development, including build and test automation
- Development, debugging and optimization of systems using distributed processing
- Parasitic extraction for advanced semiconductor nodes
- Transistor-level simulation or analysis
- Gate level or RTL power analysis and/or optimization
- IC physical design

#### **Registration Link:**

<https://chp.tbe.taleo.net/chp02/ats/careers/apply.jsp?org=ANSYS&cws=1&rid=6526>