

<b>OPERATIONAL AMPLIFIERS AND LINEAR ICs (Foundation Course)</b> <b>B.E., IV Semester, Electrical and Electronics Engineering [As per</b> <b>Choice Based Credit System (CBCS) scheme]</b>			
Course Code	17EE46	CIE Marks	40
Number of Lecture Hours/Week	03	SEE Marks	60
Total Number of Lecture Hours	40	Exam Hours	03
<b>Credits - 03</b>			
<b>Course Objectives:</b>			
<ul style="list-style-type: none"> <li>• To understand the basics of Linear ICs such as Op-amp, Regulator, Timer &amp; PLL.</li> <li>• To learn the designing of various circuits using linear ICs.</li> <li>• To use these linear ICs for specific applications.</li> <li>• To understand the concept and various types of converters.</li> <li>• To use these ICs, in Hardware projects.</li> </ul>			
<b>Module-1</b>			<b>Teaching Hours</b>
<b>Operational amplifiers:</b> Introduction, Block diagram representation of a typical Op-amp, schematic symbol, characteristics of an Op-amp, ideal op-amp, equivalent circuit, ideal voltage transfer curve, open loop configuration, differential amplifier, inverting & non –inverting amplifier, Op-amp with negative feedback(excluding derivations). <b>General Linear Applications:</b> A.C. amplifier, summing, scaling & averaging amplifier, inverting and non-inverting configuration, Instrumentation amplifier.			<b>08</b>
<b>Revised Bloom's Taxonomy Level</b>	L <sub>1</sub> – Remembering, L <sub>2</sub> – Understanding, L <sub>3</sub> – Applying, L <sub>4</sub> – Analysing.		
<b>Module-2</b>			
<b>Active Filters:</b> First & Second order high pass & low pass Butterworth filters. Band pass filters, all pass filters. <b>DC Voltage Regulators:</b> voltage regulator basics, voltage follower regulator, adjustable output regulator, LM317 & LM337 Integrated circuits regulators.			<b>08</b>
<b>Revised Bloom's Taxonomy Level</b>	L <sub>1</sub> – Remembering, L <sub>2</sub> – Understanding, L <sub>3</sub> – Applying, L <sub>4</sub> – Analysing.		
<b>Module-3</b>			
<b>Signal generators:</b> Triangular / rectangular wave generator, phase shift oscillator, saw tooth oscillator. <b>Comparators &amp; Converters:</b> Basic comparator, zero crossing detector, inverting & non-inverting Schmitt trigger circuit, voltage to current converter with grounded load, current to voltage converter and basics of voltage to frequency and frequency to voltage converters.			<b>08</b>
<b>Revised Bloom's Taxonomy Level</b>	L <sub>1</sub> – Remembering, L <sub>2</sub> – Understanding, L <sub>3</sub> – Applying, L <sub>4</sub> – Analysing.		
<b>Module-4</b>			
<b>Signal processing circuits:</b> Precision half wave & full wave rectifiers <b>A/D &amp; D/A Converters:</b> Basics, R–2R D/A Converter, Integrated circuit 8-bit D/A, successive approximation ADC, linear ramp ADC ■			<b>08</b>
<b>Revised Bloom's Taxonomy Level</b>	L <sub>1</sub> – Remembering, L <sub>2</sub> – Understanding, L <sub>3</sub> – Applying, L <sub>4</sub> – Analysing.		
<b>Module-5</b>			
<b>Phase Locked Loop (PLL):</b> Basic PLL, components, performance factors. <b>Timer:</b> Internal architecture of 555 timer, Mono stable multivibrators and applications.			<b>08</b>
<b>Revised Bloom's Taxonomy Level</b>	L <sub>1</sub> – Remembering, L <sub>2</sub> – Understanding, L <sub>3</sub> – Applying, L <sub>4</sub> – Analysing.		

<b>ELECTRICAL AND ELECTRONIC MEASUREMENTS (Foundation Course)</b> <b>B.E., IV Semester, Electrical and Electronics Engineering [As per</b> <b>Choice Based Credit System (CBCS) scheme]</b>				
<b>17EE46 OPERATIONAL AMPLIFIERS AND LINEAR ICs (Foundation Course) (continued)</b>				
<b>Course Outcomes:</b> At the end of the course the student will be able to:				
<ul style="list-style-type: none"> <li>• Describe the characteristics of ideal and practical operational amplifier.</li> <li>• Design filters and signal generators using linear ICs.</li> <li>• Demonstrate the application of Linear ICs as comparators and rectifiers.</li> <li>• Use ICs in the electronic projects.</li> </ul>				
<b>Graduate Attributes (As per NBA)</b> Engineering Knowledge, Design / development of solutions, Conduct investigations of complex Problems.				
<b>Question paper pattern:</b>				
<ul style="list-style-type: none"> <li>• The question paper will have ten questions.</li> <li>• Each full question is for 16 marks.</li> <li>• There will be 2 full questions (with a maximum of four sub questions in one full question) from each module.</li> <li>• Each full question with sub questions will cover the contents under a module.</li> <li>• Students will have to answer 5 full questions, selecting one full question from each module.</li> </ul>				
<b>Text Books:</b>				
1	Op-Amps and Linear Integrated Circuits	Ramakant A Gayakwad	Pearson	4 <sup>th</sup> Edition 2015
2	Operational Amplifiers and Linear ICs	David A. Bell	Oxford	3 <sup>rd</sup> Edition 2011
<b>Reference Books:</b>				
3	Linear Integrated Circuits; Analysis, Design and Applications	B. Somanthan Nair	Wiley India	2013
4	Linear Integrated Circuits	S. Salivahanan, et al	McGraw Hill	2 <sup>nd</sup> Edition, 2014
5	Operational Amplifiers and Linear Integrated Circuits	K. Lal Kishore	Pearson	1 <sup>st</sup> Edition, 2012